

Examiner Lourdes Cruz

09/487,421

PROPOSED AMENDMENTS TO CLAIMS 13 AND 14

13. (three times amended) A lead frame for an integrated circuit chip having a frame-engaging bottom surface, comprising a plurality of [unitary] sidebars having a uniform width, each of said sidebars having an inner side and an outer side, said sidebars defining a central aperture, said frame being sized to be accommodated entirely within corresponding outer edges of the circuit chip, each side bar having an upper chip-supporting surface for engaging the bottom surface of the chip.

14. (twice amended) A lead frame for an integrated circuit chip having a frame engaging bottom surface comprising:

[an apertured] a generally circular frame having a central aperture [said frame being generally circular and defining] forming an inner edge and an outer [defining a] peripheral edge [and having] defining a contact surface having a uniform width for securing the chip thereto, said edge being disposed within an outer chip edge for minimizing fillet formation.

RESPECTFULLY SUBMITTED,

JOHN DE LUCA REG. NO. 25505